40G QSFP+ Active Optical Cable

Features

- Four-channel full-duplex active optical cable
- Transmission data rate up to 11.3Gbit/s per channel
- Reliable VCSEL array technology using multimode fiber
- Available in standard lengths of 3, 5, 10, 15, 20, 30, 50,100m
- Low power consumption <1.5W
- Operating case temperature 0°C to +70°C
- 3.3V power supply voltage
- RoHS 6 compliant
- Hot Pluggable QSFP form factor

Applications

- Infiniband QDR/DDR/SDR
- Datacenter
- 40G Ethernet
- 4G/8G/10G Fibre Channel

Description

The Flexlink QSFP+ active optic cables are a high performance, low power consumption, long reachinterconnect solution supporting InfiniBand QDR/DDR/SDR,12.5G/10G/8G/4G/2G fiber channel ,PCIe and SAS. It is compliant with the QSFP MSA and IEEE P802.3ba. Flexlink QSFP AOC is an assembly of 4 full-duplex lanes, where each lane iscapable of transmitting data at rates up to 11.3Gb/s, providing an aggregated rate of 45.2Gb/s. Flexlink QSFP+AOC is one kind of parallel transceiver which provides increased port density and total system cost savings.

Absolute Maximum Ratings

The operation in excess of any absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Unit	Note
Storage Temperature	TST	-40	85	degC	
Relative Humidity(non-condensing)	RH	0	85	%	
Operating Case Temperature	TOPC	0	70	degC	
Supply Voltage	VCC	-0.3	3.6	V	
Input Voltage	Vin	-0.3	Vcc+0.3	V	

Recommended Operating Conditions and Supply Requirements

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	TOPC	0		70	degC
Power Supply Voltage	VCC	3.13	3.3	3.47	V
Power Consumption		-		1.5	W
Data Rate	DR	1	10.3	11.3	Gbps
Data Speed Tolerance	ΔDR	-100		+100	ppm
Link Distance with OM3 fiber	D	0		100	m

Electrical Specifications

Parameter	Symbol	Min	Typical	Max	Unit
Differential input impedance	Zin	90	100	110	ohm
Differential Output impedance	Zout	90	100	110	ohm
Differential input voltage amplitude	Vin	300		1100	mVp-p
Differential output voltage amplitude	Vout	500		800	mVp-p
Bit Error Rate	BR			E-12	
Input Logic Level High	VIH	2.0		VCC	V
Input Logic Level Low	VIL	0		0.8	V
Output Logic Level High	VOH	VCC-0.5		VCC	V
Output Logic Level Low	VOL	0		0.4	V

Pin Descriptionsz

PIN	Logic	Symbol	Name/Description	Note
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	1
8	LVTLL-I	ModSelL	Module Select	
9	LVTLL-I	ResetL	Module Reset	
10		VccRx	+ 3.3V Power Supply Receiver	2
11	LVCMOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVCMOS-I/O	SDA	2-Wire Serial Interface Data	